

Appln No. 10/623,839  
Amdt date July 7, 2004

**Amendments to the Specification:**

Please replace pages 189-192 of the specification. The replace sheets are attached hereto.

Request Type				Count/Offset/ Type				Description	Request Type Code
D7	D6	D5	D4	D3	D2	D1	D0		
'b0000								Linear Gwords Read Access	
				0	0	0	0	16 Gwords	LG_16R
				0	0	0	1	1 Gword	LG_1R
				0	0	1	0	2 Gwords	LG_2R
				n	n	n	n	N Gwords	LG_NR
				1	1	1	1	15 Gwords	LG_15R
'b0001								Linear Gwords Write Access (DQM=0)	
				0	0	0	0	16 Gwords	LG_16R
				0	0	0	1	1 Gword	LG_1R
				0	0	1	0	2 Gwords	LG_2R
				n	n	n	n	N Gwords	LG_NR
				1	1	1	1	15 Gwords	LG_15R
'b0010								Gword Lower Write Access	
				0	0	0	0	Write Byte #0 only	GL_0W
				0	0	0	1	Write Byte #1 to Byte #0	GL_1W
				0	0	1	0	Write Byte #2 to Byte #0	GL_2W
				n	n	n	n	Write Byte #N to Byte #0	GL_NW
				1	1	1	1	Write Byte #15 to Byte #0	GL_15W
'b0011								Gword Upper Write Access	
				0	0	0	0	Write Byte #15 to Byte #0	GU_0W
				0	0	0	1	Write Byte #15 to Byte #1	GU_1W
				0	0	1	0	Write Byte #15 to Byte #2	GU_2W

Request Type	Count/Offset/ Type				Description	Request Type Code
	n	n	n	n	Write Byte #15 to Byte #N	GU_NW
	1	1	1	1	Write Byte #15 only	GU_15W
b0100					Single Byte Write Access	
	0	0	0	0	Write Byte #0	SB_0W
	0	0	0	1	Write Byte #1	SB_1W
	0	0	1	0	Write Byte #2	SB_2W
	n	n	n	n	Write Byte #N	SB_NW
	1	1	1	1	Write Byte #15	SB_15W
b0101					Single Word Write Access	
	0	0	0	0	Write 16 bit word #0	SW_0W
	0	0	0	1	Write 16 bit word #1	SW_1W
	0	n	n	n	Write 16 bit word #N	SW_NW
	0	1	1	1	Write 16 bit word #7	SW_7W
	1	x	0	0	Write 32 bit word #0	SD_0W
	1	x	0	1	Write 32 bit word #1	SD_1W
	1	0	1	0	Write 32 bit word #2	SD_2W
	1	0	1	1	Write 32 bit word #3	SD_3W
	1	1	0	0	8 Gwords Display Write	
	1	1	0	1	Reserved	
	1	1	1	0	Refresh Command	
	1	1	1	1	Mode Register Set Command	
					Linear Graphics Writes (with client driven DQM Mask)	
	0	0	0	0	16 Gwords	LG_16WG

Request Type	Count/Offset/ Type				Description	Request Type Code
b1001	0	0	0	1	1 Gword	LG_1WG
	0	0	1	0	2 Gwords	LG_2WG
	n	n	n	n	N Gwords	LG_NWG
	1	1	1	1	15 Gwords	LG_15WG
b0110					Display Read Access	
	0	0	0	0	16 Gwords (256 pel component)	DS_16R
	0	0	0	1	1 Gword (16 pel component)	DS_1R
	0	0	1	0	2 Gwords (32 pel component)	DS_2R
	n	n	n	n	N Gwords (N x 16 pel component)	DS_NR
	1	1	1	1	15 Gwords (240 pel component)	DS_15R
Down Conversion Macroblock Prediction (Pred) and Write Access						
b1111	0	0	0	0	8 Cols, 8 Rows Pred Alternate Reads	M8x8AR
	0	0	0	1	8 Cols, 9 Rows Pred Alternate Reads	M8x9AR
	0	1	0	0	8 Cols, 4 Rows Pred Alternate Reads	M8x5AR
	0	1	0	1	8 Cols, 5 Rows Pred Alternate Reads	M8x5AR
	1	0	0	0	8 Cols, 8 Rows Pred Continuous Reads	M8x8CR
	1	0	0	1	8 Cols, 9 Rows Pred Continuous Reads	M8x9CR
	1	1	0	0	8 Cols, 8 Rows Alternate Writes	M8x8AW
	1	1	0	1	8 Cols, 16 Rows Alternate Writes	M8x16AW
	1	1	1	0	8 Cols, 8 Rows Continuous Writes	M8x8CW
	1	1	1	1	8 Cols, 16 Rows Continuous Writes	M8x16CW
Macroblock Prediction (Pred) and Write Access						
	0	0	0	0	16 Cols, 8 Rows Pred Alternate Reads	M16x8AR

Request Type	Count/Offset/ Type				Description	Request Type Code
b0111	0	0	0	1	16 Cols, 9 Rows Pred Alternate Reads	M16x9AR
	0	0	1	0	32 Cols, 8 Rows Pred Alternate Reads	M32x8AR
	0	0	1	1	32 Cols, 9 Rows Pred Alternate Reads	M32x9AR
	0	1	0	0	16 Cols, 4 Rows Pred Alternate Reads	M16x4AR
	0	1	0	1	16 Cols, 5 Rows Pred Alternate Reads	M16x5AR
	0	1	1	0	32 Cols, 4 Rows Pred Alternate Reads	M32x4AR
	0	1	1	1	32 Cols, 5 Rows Pred Alternate Reads	M32x5AR
	1	0	0	0	16 Cols, 8 Rows Pred Cont. Reads	M16x8CR
	1	0	0	1	16 Cols, 9 Rows Pred Cont. Reads	M16x9CR
	1	0	1	0	32 Cols, 8 Rows Pred Cont. Reads	M32x8CR
	1	0	1	1	32 Cols, 9 Rows Pred Cont. Reads	M32x9CR
	1	1	0	0	16 Cols, 8 Rows Alternate Writes	M16x8AW
	1	1	0	1	16 Cols, 16 Rows Alternate Writes	M16x16AW
	1	1	1	0	16 Cols, 8 Rows Continuous Writes	M16x8CW
	1	1	1	1	16 Cols, 16 Rows Continuous Writes	M16x16CW

Table 5.1

During “linear Gwords read access” operations, as indicated in table 5.1 with a request type of ‘b0000, one to 16 Gwords (128 bits) preferably are read from memory at a time. During “linear Gwords write access” operations with a request type of ‘b0001, one to 16 Gwords preferably are written to memory at a time.